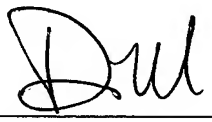
Conclusion

Prior to an examination of the application on the merits, Applicant respectfully requests entry of this preliminary amendment. The specification has been amended to correct typographical and translation errors. Applicant respectfully submits that the changes to the specification have not been made for any reason of patentability.

Attached hereto is a marked-up version of the changes made to the specification by the current amendment. The attachment is captioned "Version with markings to show changes made."

If there are any other fees due in connection with the filing of this paper, please charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. § 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully Submitted,

By: 

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

Paragraph **[0012]** has been amended as follows:

[0012] To enhance high resolution, it is necessary to provide a high-speed driving operation, thereby reducing a width of an applied gate pulse. Thus, a horizontal synchronizing signal interval is not only shortened, but also a time at which a data signal is applied to the liquid crystal cell is reduced. In other words, since a number of data signals required to be applied at a same time becomes larger as resolution increases, a time 'c' at which a gate pulse is applied is reduced. Furthermore, as a number of data signals to be applied to the liquid crystal cell increases, a switching time 'a' required for applying the data signals is increased. Thus, a charging time ~~['e']~~ **'b'** required for charging the data signals into the liquid crystal cell is shortened.

Paragraph **[0013]** has been amended as follows:

[0013] However, in the dot inversion system, if positive (+) data signals are applied to the liquid crystal cells at odd-numbered frames, negative (-) data signals are applied to the liquid crystal cells at even-numbered frames. Accordingly, a level for switching the data signal is increased since the data signals applied to the liquid crystal cells at two consecutive frames should be converted from the positive (+) polarity to the negative (-) polarity, thereby increasing the switching time 'a' of the data signal. As a result, since a time 'c' at which a gate pulse GP is applied is fixed for each resolution, and a switching time ~~['a']~~ **'a'** of the data signal is increased, a time 'b' at which the data signal is applied to the liquid crystal cell should be decreased. Accordingly, the data signal is not completely charged in the liquid crystal cell, thereby distorting color or brightness of the image.

Paragraph **[0035]** has been amended as follows:

[0035] The data driving IC 8 may include shift registers and latches. The data driving IC ~~[1]~~ **8** shifts data bits in response to a data shift clock DSC, and applies data for the data lines DL1 to DLn simultaneously in response to a data output enable signal DOE.